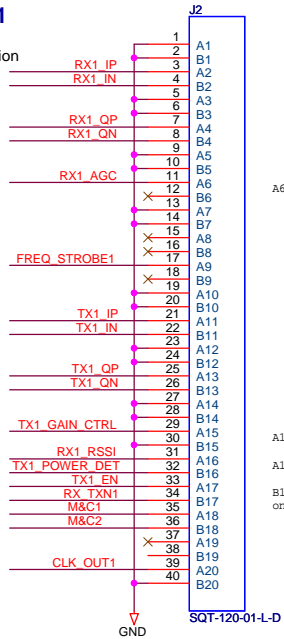


ANALOG INTERFACE 1

Differential analog I/O
40 pin Connector #1 - middle position

Receiver Interface

Transmitter Interface



A6 AUX_DAC1 typical use: rx gain control / RX_AGC1

A15 AUX_DAC2 typical use: tx gain control / TX_GAIN_CNTRL1

A16 AUX_ADC1 typical use: tx power measurement TX_POWER_DET

B16 AUX_ADC2 optional use: 2nd tx power measurement TX_POWER_DET on COM-1200 only

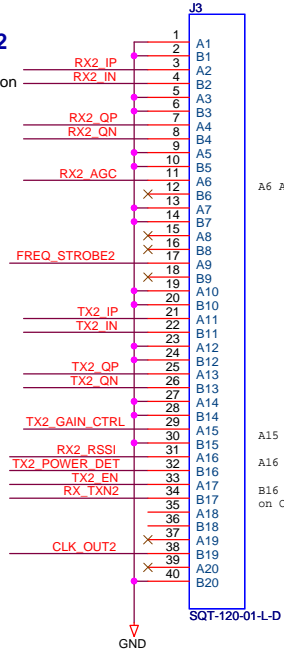
SQT-120-01-L-D

ANALOG INTERFACE 2

Differential analog I/O
40 pin Connector #2 - lowest position

Receiver Interface

Transmitter Interface



A6 AUX_DAC1 typical use: rx gain control / RX_AGC1

A15 AUX_DAC2 typical use: tx gain control / TX_GAIN_CNTRL1

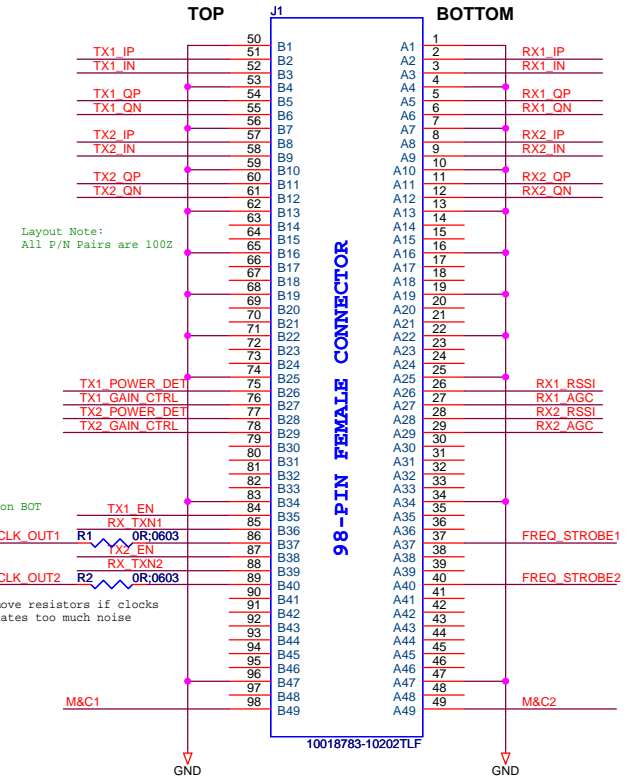
A16 AUX_ADC1 typical use: tx power measurement TX_POWER_DET

B16 AUX_ADC2 optional use: 2nd tx power measurement TX_POWER_DET on COM-1200 only

SQT-120-01-L-D

MIMO TRANSCEIVER INTERFACE

Baseband mixed analog/digital I/Os - Upper most position



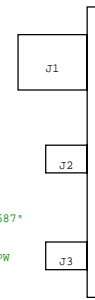
Layout Note:
All P/N Pairs are 100Z

Layout Note:
Place R1 and R2 near J1 pins on BOT

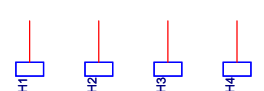
Layout Note:
Route CLK_OUTx and M&Cx away from Z traces

remove resistors if clocks creates too much noise

Connector Orientation



PCB MOUNTING HOLES



Layout Note:
Place Holes 160 mils from edges in all 4 corners

AK
Mobile Satellite Services
18221A Flower Hill Way
Gaithersburg, MD 20879
JSA

Title
COM-9109 98-Pin to Two 40 Pin Analog Adapter

Size B Document Number
Y11001

Date: Thursday, June 02, 2011 Sheet 1 of 1

Layout Note:
PCB Dimensions = 3" x 2"
Connector to Connector Y Spacing = 0.687"
J3 Y = 0.25"
4 Layers
GND Planes => In1 Solid & Top Solid/CPW

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