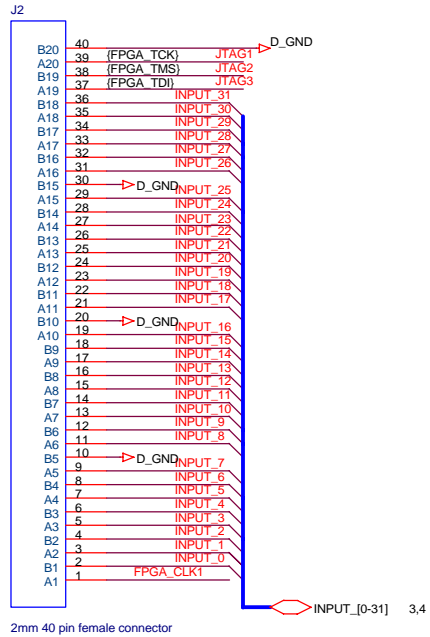


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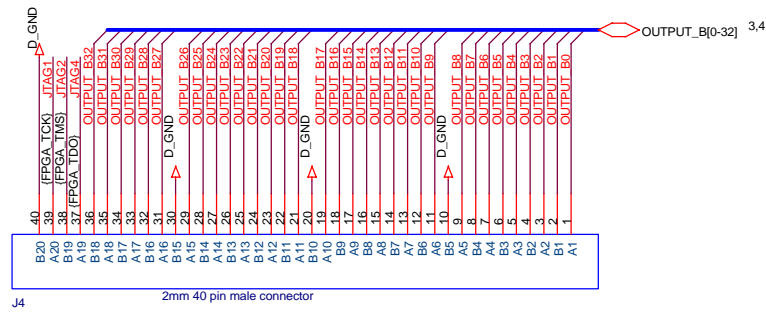
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|-----------------|------------------|
| MAIN.....1 | MICRO.....4 |
| CONNECTOR.....2 | POWER.....5 |
| FPGA.....3 | TURBO CODE.....6 |

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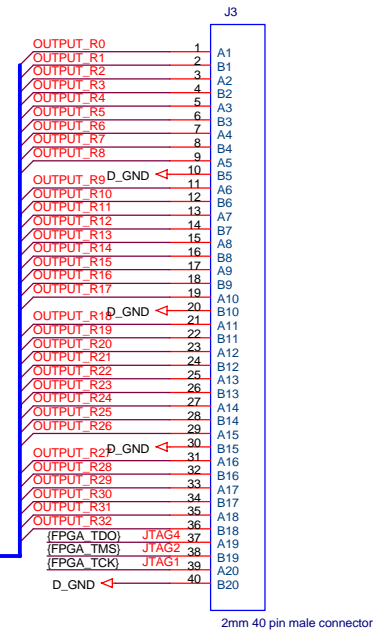
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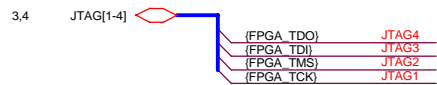
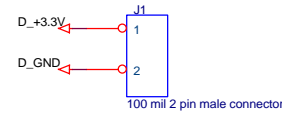
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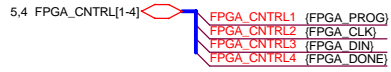
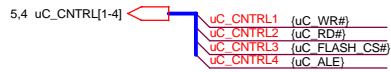
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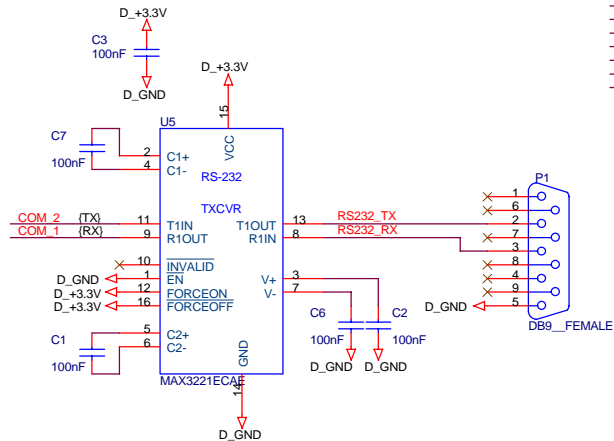
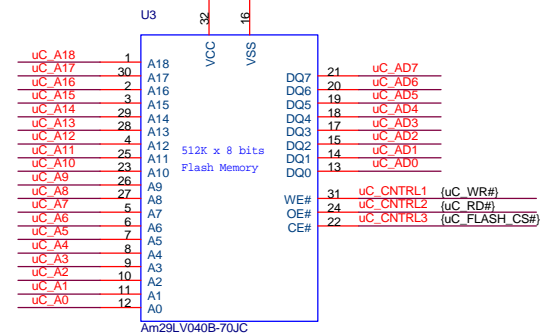
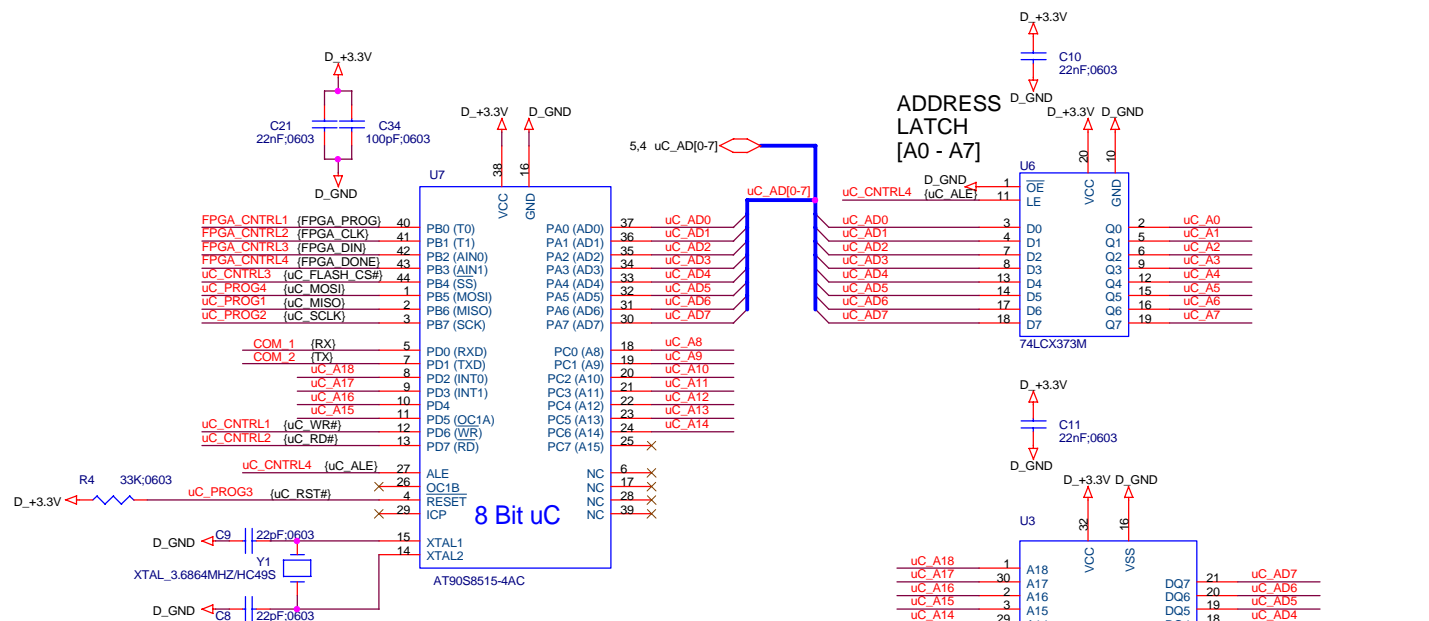
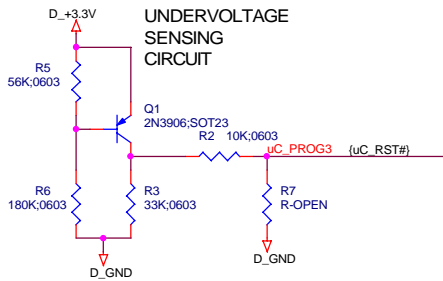
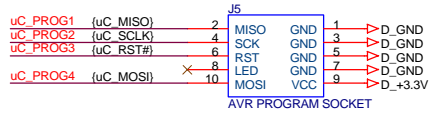
FPGA_CLK1



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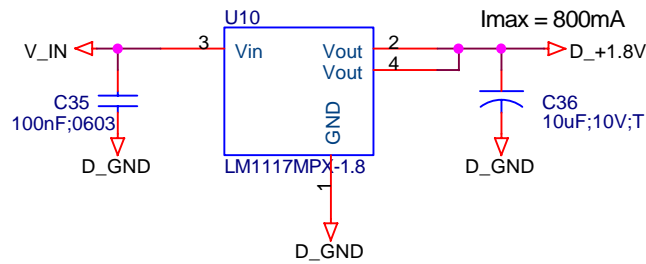
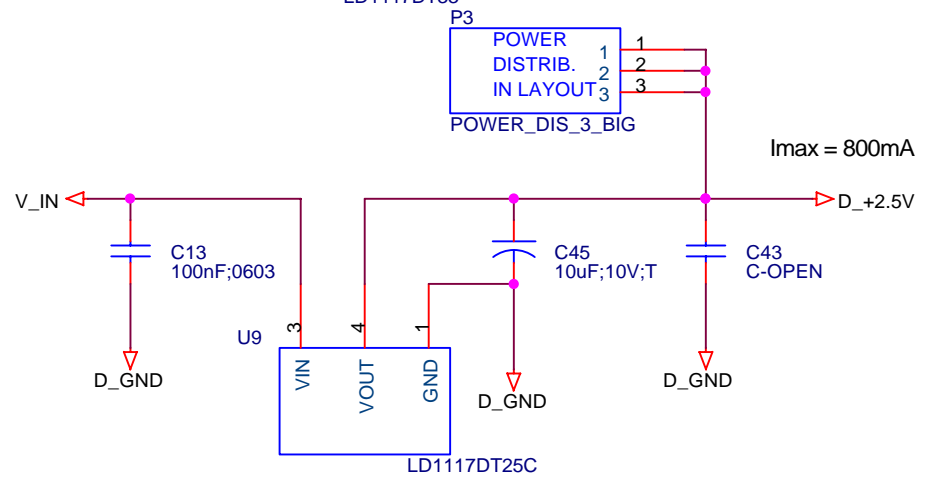
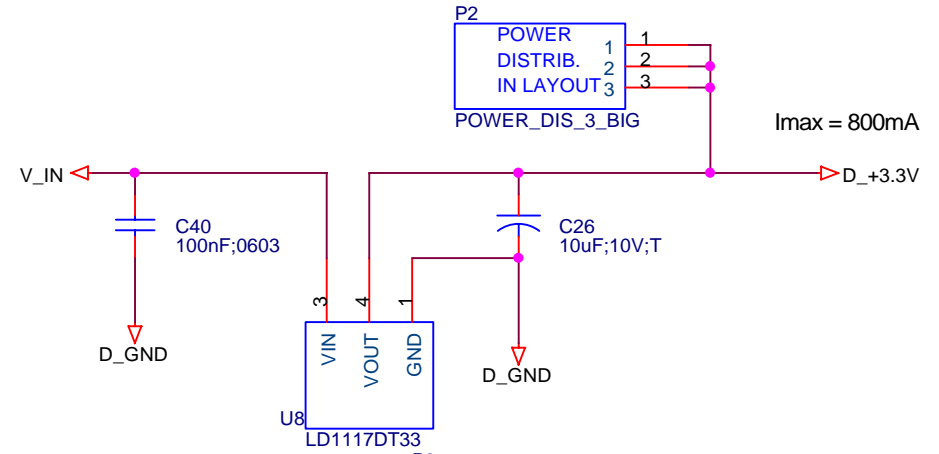
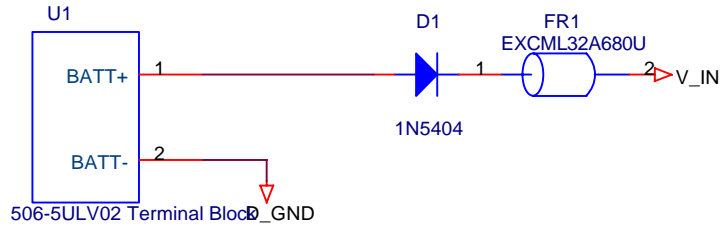
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+5 V Supply

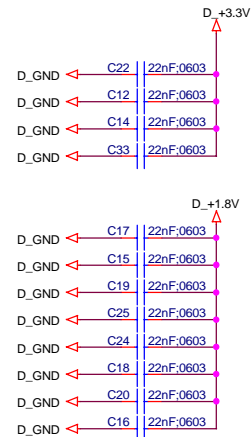
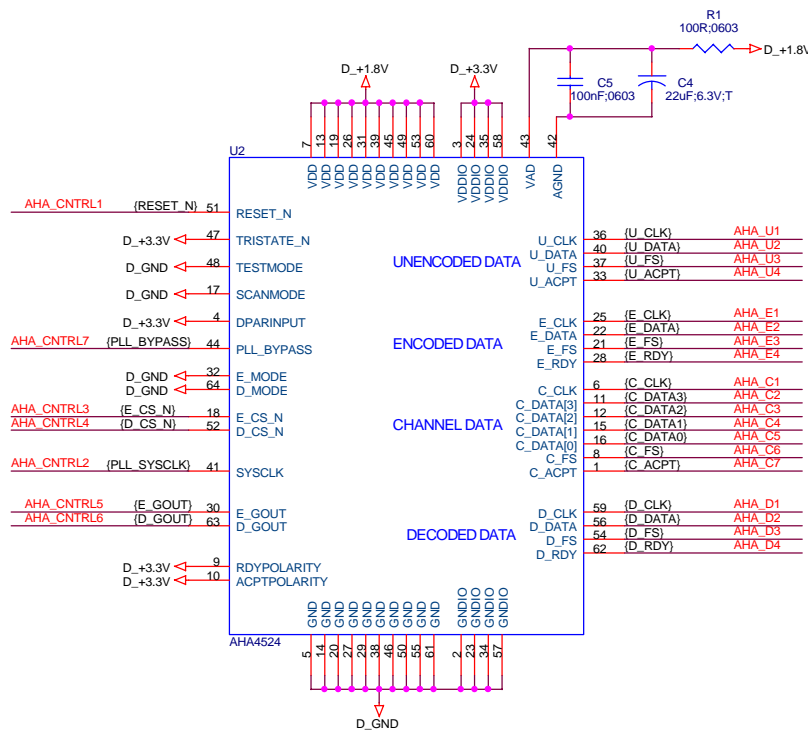
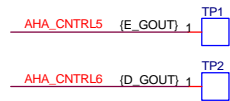
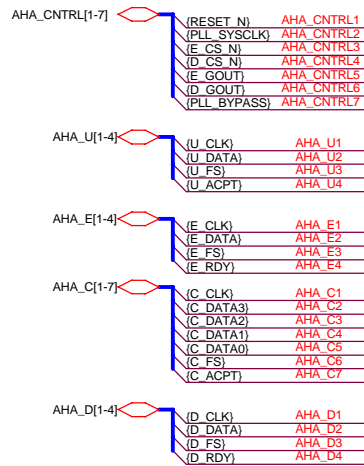


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