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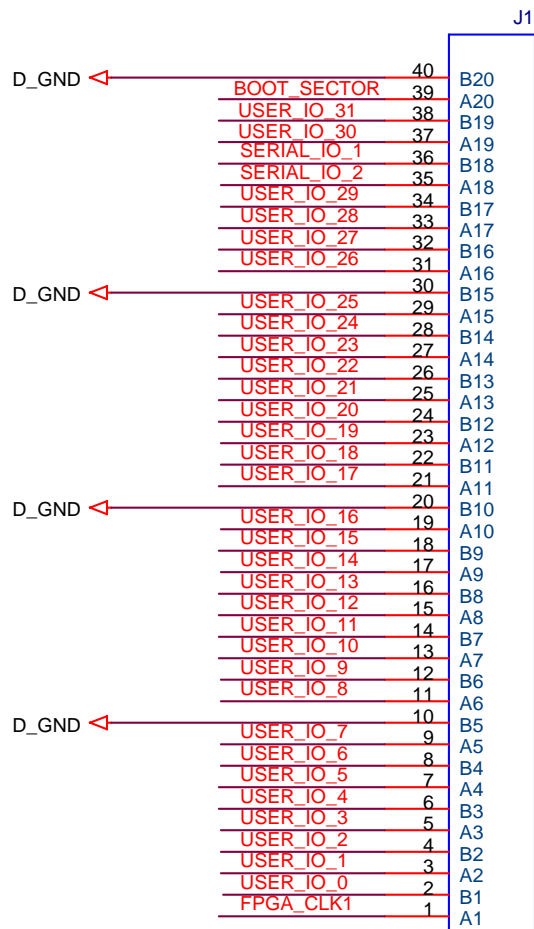
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Title
COM 1300 / Main

Size A	Document Number Y04002	Rev 1
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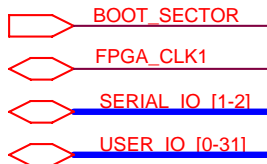
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Manually connecting A20 (BOOT SECTOR) with B20 will reload the default FPGA configuration file saved in the flash memory.



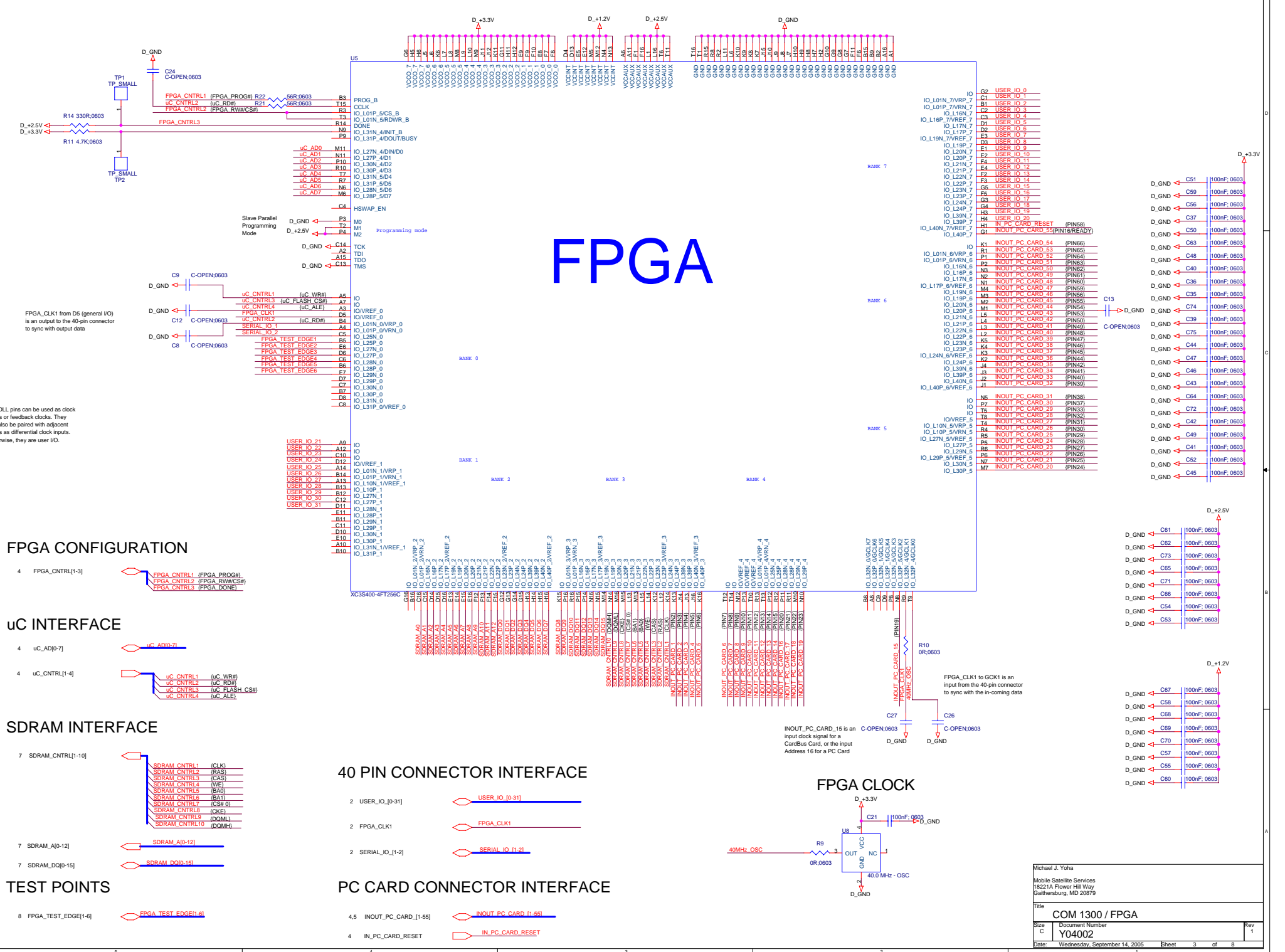
SQT-120-01-L-D-RA

- 4 BOOT_SECTOR
- 3 FPGA_CLK1
- 3 SERIAL_IO_[1-2]
- 3 USER_IO_[0-31]



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FPGA



FPGA CONFIGURATION

uC INTERFACE

SDRAM INTERFACE

40 PIN CONNECTOR INTERFACE

PC CARD CONNECTOR INTERFACE

FPGA CLOCK

TEST POINTS

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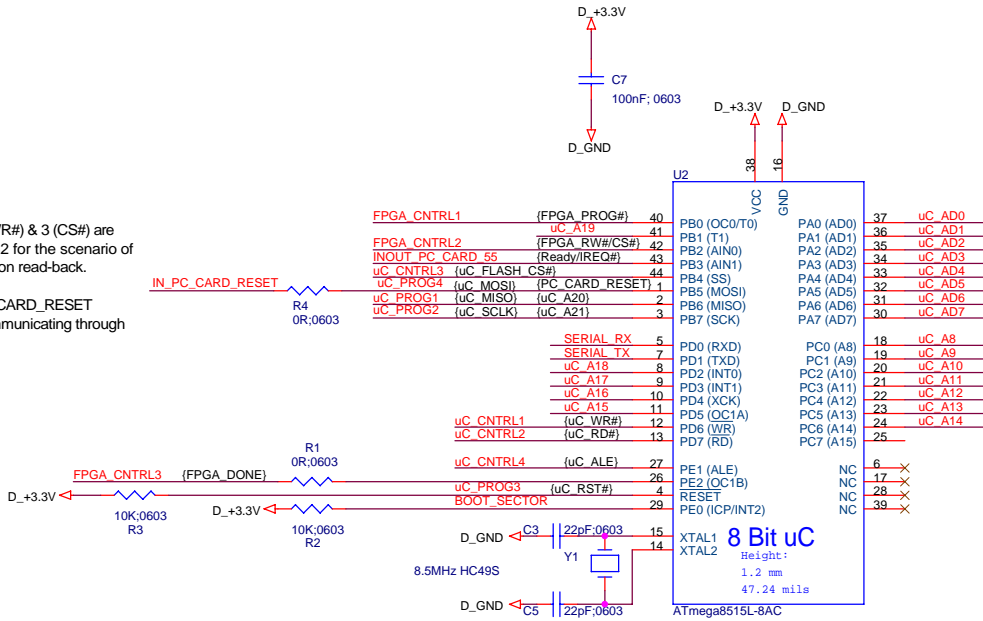
Title
COM 1300 / FPGA

Size C Document Number
Y04002

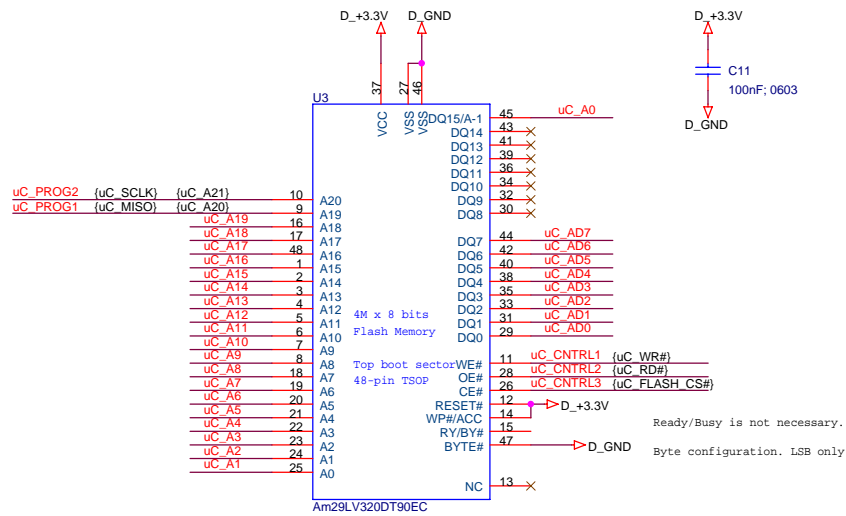
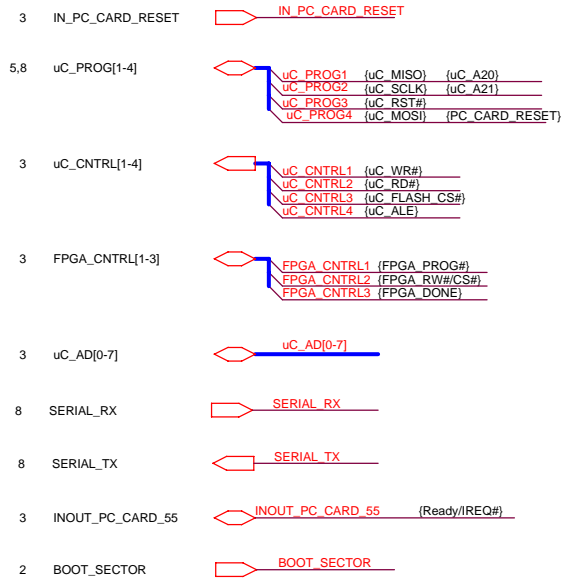
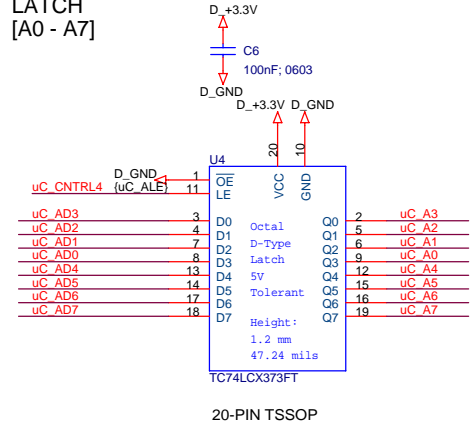
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FPGA_CNTRL2 (WR#) & 3 (CS#) are connected with PB2 for the scenario of single FPGA and non read-back.

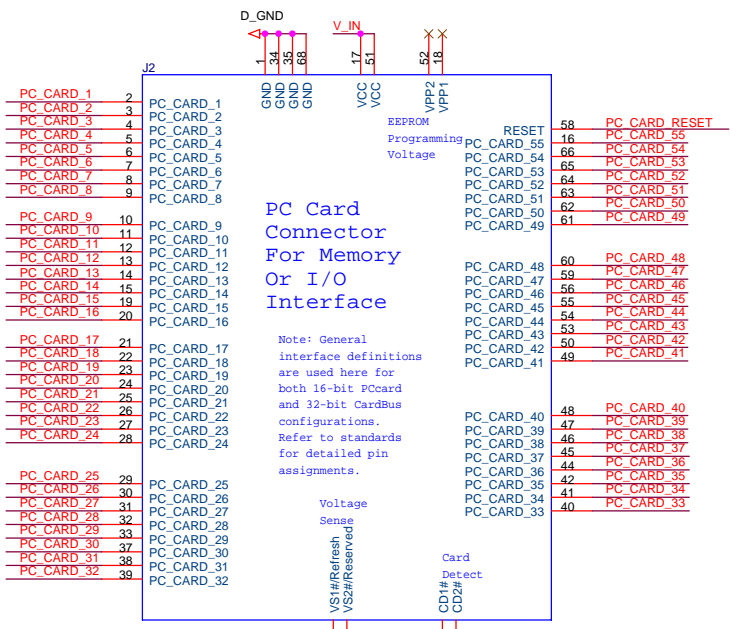
PB5 is used for PC_CARD_RESET when uC is not communicating through SPI channel.



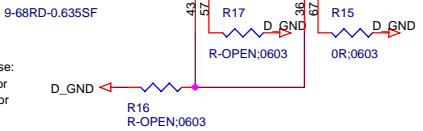
ADDRESS LATCH [A0 - A7]



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For card detect and voltage sense: populate R16 with 0 ohm resistor for 16-bit PC Card; leave open for 32-bit CardBus Card.



PC CARD 1	16	A	1	INOUT PC CARD 1
PC CARD 2	15	B	2	INOUT PC CARD 2
PC CARD 30	14	B	3	INOUT PC CARD 30
PC CARD 3	13	C	4	INOUT PC CARD 3
PC CARD 31	12	C	5	INOUT PC CARD 31
PC CARD 4	11	D	6	INOUT PC CARD 4
PC CARD 32	10	D	7	INOUT PC CARD 32
PC CARD 5	9	E	8	INOUT PC CARD 5

PC CARD 33	16	A	1	INOUT PC CARD 33
PC CARD 6	15	B	2	INOUT PC CARD 6
PC CARD 34	14	B	3	INOUT PC CARD 34
PC CARD 7	13	C	4	INOUT PC CARD 7
PC CARD 35	12	C	5	INOUT PC CARD 35
PC CARD 8	11	D	6	INOUT PC CARD 8
PC CARD 9	10	D	7	INOUT PC CARD 9
PC CARD 36	9	E	8	INOUT PC CARD 36

PC CARD 10	16	A	1	INOUT PC CARD 10
PC CARD 37	15	B	2	INOUT PC CARD 37
PC CARD 11	14	B	3	INOUT PC CARD 11
PC CARD 38	13	C	4	INOUT PC CARD 38
PC CARD 12	12	C	5	INOUT PC CARD 12
PC CARD 39	11	D	6	INOUT PC CARD 39
PC CARD 13	10	D	7	INOUT PC CARD 13
PC CARD 40	9	E	8	INOUT PC CARD 40

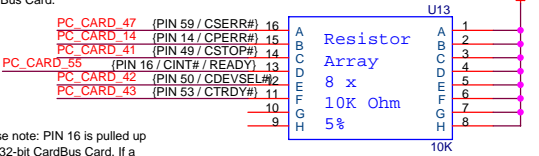
PC CARD 14	16	A	1	INOUT PC CARD 14
PC CARD 41	15	B	2	INOUT PC CARD 41
PC CARD 15	14	B	3	INOUT PC CARD 15
PC CARD 42	13	C	4	INOUT PC CARD 42
PC CARD 16	12	C	5	INOUT PC CARD 16
PC CARD 43	11	D	6	INOUT PC CARD 43
PC CARD 17	10	D	7	INOUT PC CARD 17
PC CARD 44	9	E	8	INOUT PC CARD 44

PC CARD 17	16	A	1	INOUT PC CARD 17
PC CARD 45	15	B	2	INOUT PC CARD 45
PC CARD 18	14	B	3	INOUT PC CARD 18
PC CARD 46	13	C	4	INOUT PC CARD 46
PC CARD 19	12	C	5	INOUT PC CARD 19
PC CARD 20	11	D	6	INOUT PC CARD 20
PC CARD RESE	10	D	7	IN PC CARD RESET
PC CARD 21	9	E	8	INOUT PC CARD 21

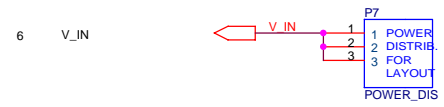
PC CARD 47	16	A	1	INOUT PC CARD 47
PC CARD 22	15	B	2	INOUT PC CARD 22
PC CARD 48	14	B	3	INOUT PC CARD 48
PC CARD 23	13	C	4	INOUT PC CARD 23
PC CARD 49	12	C	5	INOUT PC CARD 49
PC CARD 24	11	D	6	INOUT PC CARD 24
PC CARD 50	10	D	7	INOUT PC CARD 50
PC CARD 25	9	E	8	INOUT PC CARD 25

PC CARD 51	16	A	1	INOUT PC CARD 51
PC CARD 26	15	B	2	INOUT PC CARD 26
PC CARD 52	14	B	3	INOUT PC CARD 52
PC CARD 27	13	C	4	INOUT PC CARD 27
PC CARD 53	12	C	5	INOUT PC CARD 53
PC CARD 28	11	D	6	INOUT PC CARD 28
PC CARD 54	10	D	7	INOUT PC CARD 54
PC CARD 29	9	E	8	INOUT PC CARD 29

A few control signals that should be pulled up for the CardBus Card.



Please note: PIN 16 is pulled up for a 32-bit CardBus Card. If a 16-bit PC Card is installed, it will be set by the microcontroller.



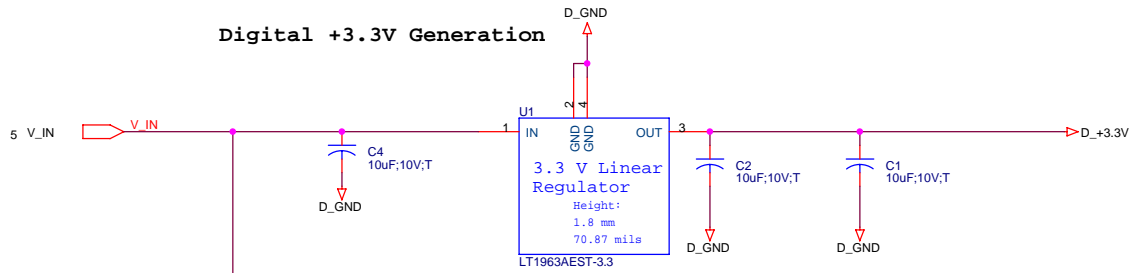
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Title: **COM 1300 / PC Card Connector**

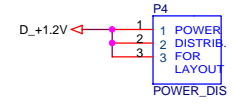
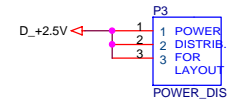
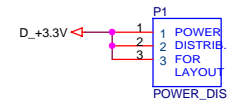
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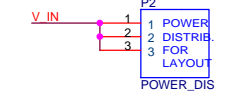
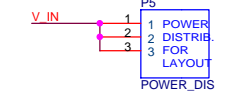
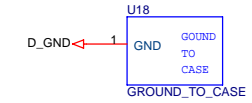
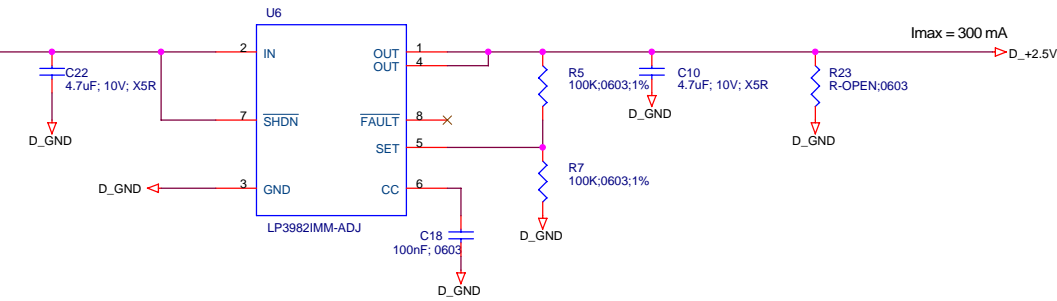
Digital +3.3V Generation



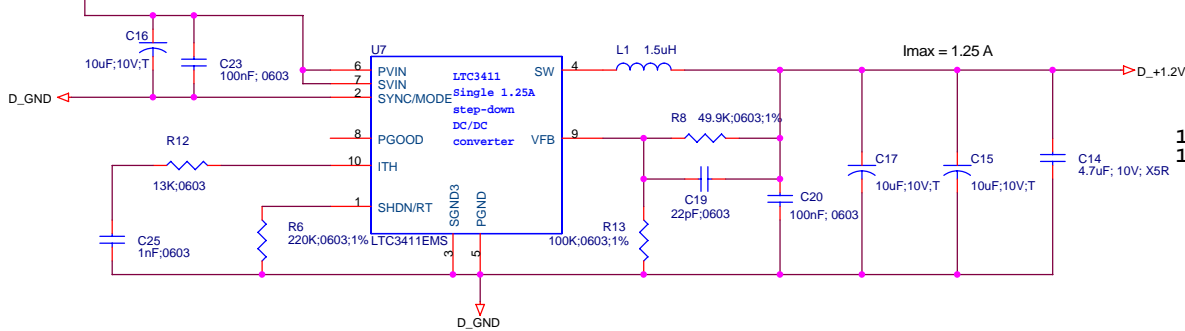
1.5 A
 Dropout Voltage @ 200 mA: 100 mV
 Dropout Voltage @ 400 mA: 150 mV
 Dropout Voltage @ 600 mA: 180 mV
 Dropout Voltage @ 800 mA: 220 mV



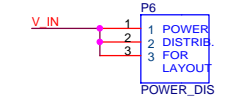
Digital +2.5V Generation



Digital +1.2V Generation

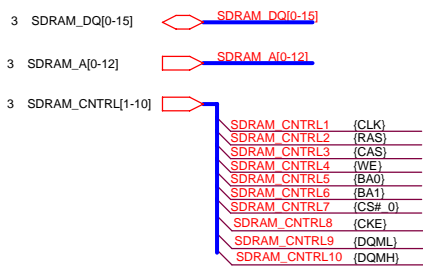
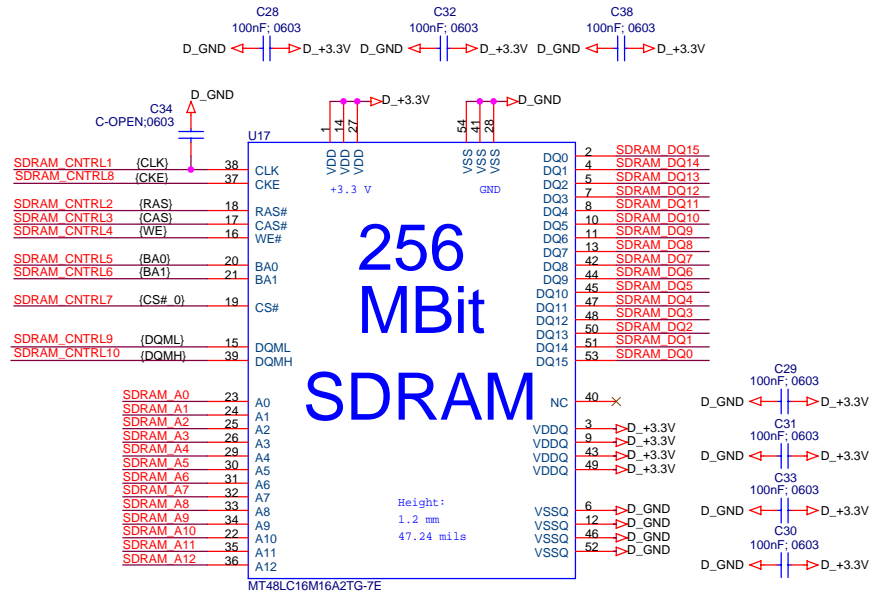


1.2 V \pm 7.5%
 1.25 A Max

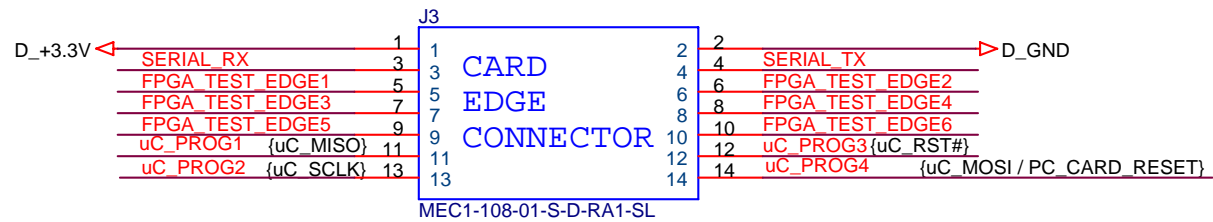


- * skip pulse mode for minimum noise
- * 1.6A max peak output current
- * VREF = 0.8V
- * 1.6 MHz switching
- * P-P output voltage ripple < 0.03V for ESR (Cout) < 150 mohm

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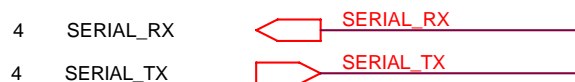


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Please note: Serial link is LVTTTL,
NOT RS232.

SERIAL INTERFACE



uC PROGRAMMING



FPGA TEST POINTS



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